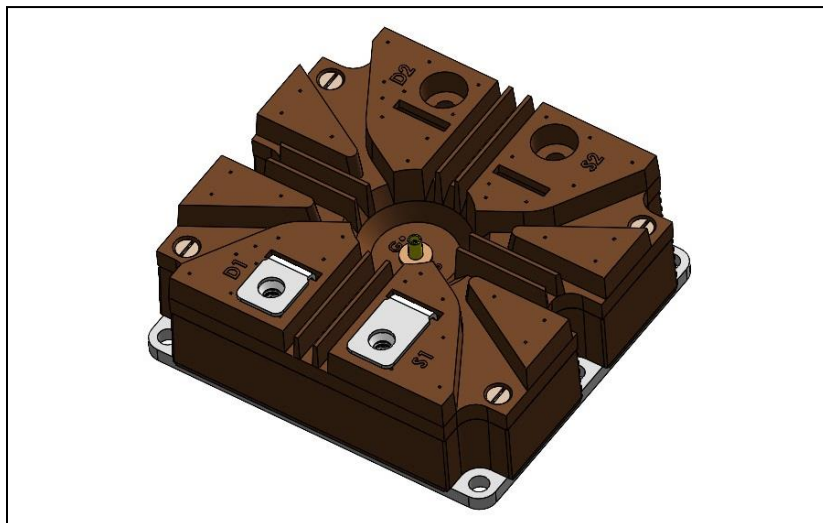
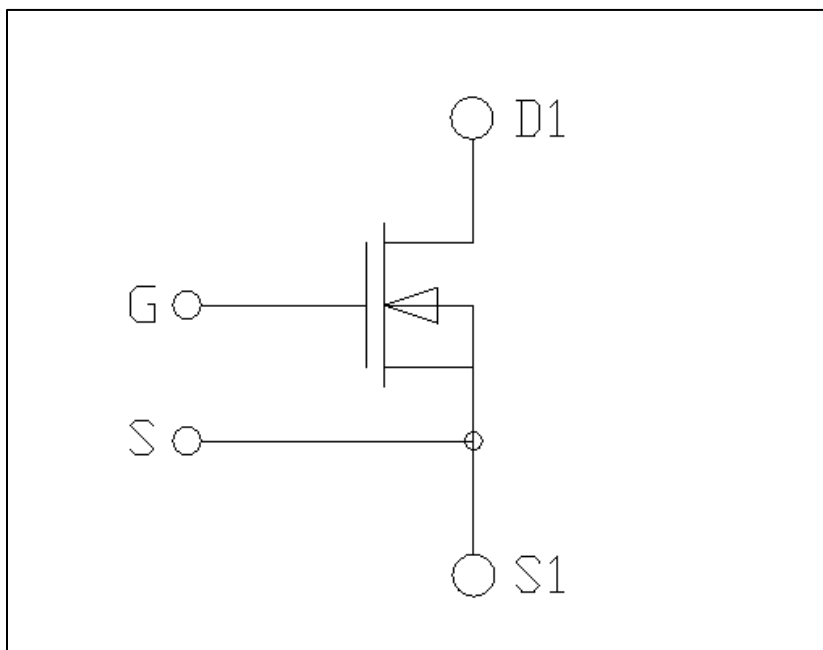


**High Voltage Silicon Carbide
MOSFET Module
108 Amperes / 10kV / 50 mΩ**

**Single SiC MOSFET Module
108 Amperes / 10kV**

**Description:**

Powerex HV Silicon Carbide MOSFET Modules are designed for use in high voltage applications. Each module consists of one MOSFET Silicon Carbide Transistor. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- ☐ Junction Temperature: 175°C
- ☐ Low $R_{DS(on)}$
- ☐ High Speed Switching
- ☐ Temperature-Independent Switching
- ☐ Low Stray Inductance (26nH)
- ☐ 15kV Partial Discharge
- ☐ 20kV Isolation Voltage
- ☐ Aluminum Nitride Isolation
- ☐ Copper Baseplate

Applications:

- ☐ Grid tied Solar Inverters
- ☐ Medium Voltage Motor Drives
- ☐ Power Distribution in Data Centers
- ☐ Power Distribution in Factories
- ☐ Railway Application

Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	QJSB014SB1	Units
Drain-Source Voltage (G-S Short)	V_{DS}	10000	Volts
Gate-Source Voltage, DC, D-S short	V_{GSmax}	-9 / +19	Volts
Drain Current (Continuous) at $T_c=61^\circ\text{C}^{*1}$	I_D	108	Amperes
Drain Current (Pulse, Repetitive) *2 , $T_{vj}=150^\circ\text{C}^{*3}$	$I_{D(pulse)}$	216	Amperes
Maximum Power Dissipation ($T_c=25^\circ\text{C}$, $T_j < 175^\circ\text{C}$) *1	P_D	1500	Watts
Maximum Junction Temperature	T_{jmax}	175	$^\circ\text{C}$
Operating Junction Temperature, Continuous operation (under switching)	T_{jop}	-40 to 150	$^\circ\text{C}$
Maximum Case Temperature *1	T_{cmax}	125	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 125	$^\circ\text{C}$
Mounting Torque, M6 Mounting Screws	—	5.5	N-m
Terminal Connection Torque, M8 Terminal Screws	—	10	N-m
Module Weight (Typical)	—	1600	Grams
Isolation Voltage	V_{ISO}	20	kVolts
Partial Discharge Extinction Voltage, RMS, Sinusoidal, $f = 60\text{Hz}$, $Q_{PD} \leq 10\text{pC}$	V_e	15	kVolts

*1 Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink under the chips.*2 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(MAX)}$ rating.*3 Junction temperature (T_{vj}) should not increase beyond $T_{j(MAX)}$ rating.**DC Characteristics, $T_j=25^\circ\text{C}$ unless otherwise specified**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain Source Leakage Current	I_{DSS}	$V_{DS}=10\text{kV}$, $V_{GS}=0\text{V}$	-	-	1.0	mA
Gate Source Leakage Current	I_{GSS}	$V_{DS}=0\text{V}$, $V_{GS}=15\text{V}$	-	-	60	nA
Recommended Gate Source Voltage	V_{GS}		-	-5/+15	-	Volts
Gate Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=10\text{V}$, $I_D=6\text{mA}$	2.5	3.2	-	Volts
Drain Source On-Resistance (chip)	$R_{DS(on)}$	$V_{GS}=15\text{V}$, $I_D=90\text{A}$	-	50	58.4	mΩ
		$T_j=150^\circ\text{C}$	-	158.4	-	mΩ
Internal Gate Source Series Resistance	R_g	Per Switch	-	1.2	-	Ω
Stray Inductance	L_s	Between Terminal D1,D2	-	26	-	nH
		and Terminal S1,S2				

Dynamic Characteristics, $T_J=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=1000V$	-	TBD	-	nF
Output Capacitance	C_{OSS}		-	TBD	-	nF
Reverse Transfer Capacitance	C_{RSS}		-	TBD	-	nF
Turn-On Delay Time	$t_{D(on)}$	$V_{DD}=6kV, V_{GS}=-5/+15V$ $I_D=90A, R_G=1\Omega, T_J=150^\circ\text{C}$	-	TBD	-	ns
Rise Time	t_R		-	TBD	-	ns
Turn-Off Delay Time	$t_{D(off)}$		-	TBD	-	ns
Fall Time	t_F	Inductive Load, per Pulse	-	TBD	-	ns
Turn-On Energy	E_{on}	$V_{DD}=6kV, V_{GS}=\pm 15V$	-	TBD	-	mJ
Turn-Off Energy	E_{off}	$I_D=90A, R_G=6.7\Omega, T_J=150^\circ\text{C}$ Inductive Load, per Pulse	-	TBD	-	mJ
Total Gate Charge	Q_G	$V_{DD}=6kV, V_{GS}=-5/+15V$ $I_D=90A, T_J=25^\circ\text{C}$	-	TBD	-	nC

Body Diode, $T_J=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Diode Forward Voltage	V_{SD}	$V_{GS}=-15V, I_S=90A$	-	8.0	-	V
		$T_J=150^\circ\text{C}$	-	11.0	-	V

Thermal Resistance Characteristics

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	$R_{th(j-c)}$	Per MOSFET	-	-	0.06	$^\circ\text{C/W}$
Contact Thermal Resistance	$R_{th(c-s)}$	Per Module, Thermal Grease Applied $\lambda=1 \text{ W/(m-K)}, D_{(c-s)} = 80\mu\text{m}$	-	0.007	-	$^\circ\text{C/W}$

