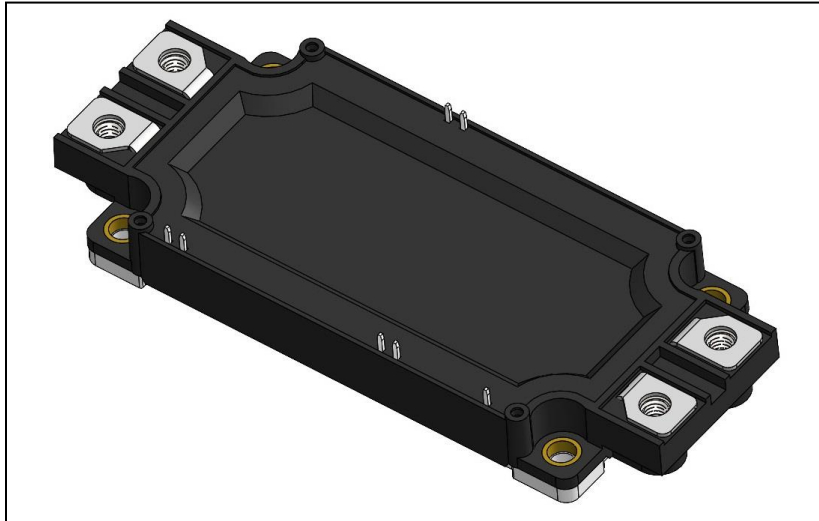


**Silicon Carbide
Dual MOSFET Module
200 Amperes / 1700 Volts / 7.3 mΩ**



**Dual SiC MOSFET Module
200 Amperes / 1700 Volts**

Description:

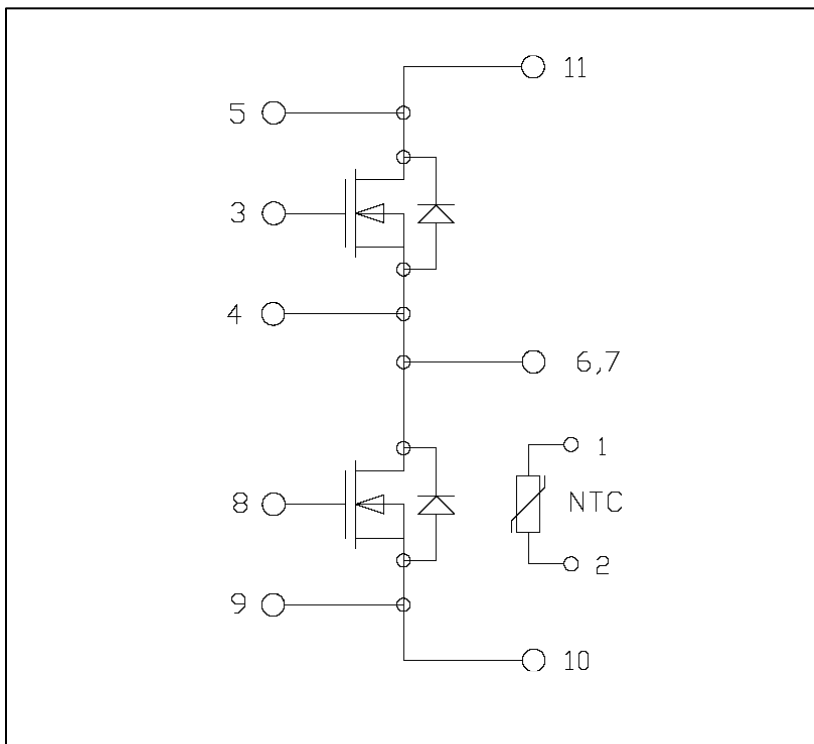
Powerex Silicon Carbide MOSFET Modules are designed for use in high frequency applications. Each module consists of two MOSFET Silicon Carbide Transistors with each transistor having a reverse connected super-fast recovery free-wheel silicon carbide Schottky diode. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- Junction Temperature: 175°C
- Industry Leading $R_{DS(on)}$
- High Speed Switching
- Low Switching Losses
- Low Capacitance
- Low Drive Requirement
- High Power Density
- Zero Reverse Recovery from Diode
- Isolated Baseplate
- Aluminum Nitride Isolation

Applications:

- Energy Saving Power Systems
- High Frequency Type Power Systems
- High Temperature Power Systems



Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	QJD1720SA1	Units
Drain-Source Voltage (G-S Short)	V_{DSS}	1700	Volts
Gate-Source Voltage, DC, D-S short	V_{GSS}	± 20	Volts
Drain Current (Continuous) at $T_c=48^\circ\text{C}^{*1}$	I_D	200	Amperes
Drain Current (Pulse, Repetitive) ^{*2} , $T_{vj}=150^\circ\text{C}^{*3}$	$I_{D(pulse)}$	300	Amperes
Maximum Power Dissipation ($T_c=25^\circ\text{C}$, $T_j < 175^\circ\text{C}$) ^{*1}	P_D	820	Watts
Maximum Junction Temperature	T_{jmax}	175	$^\circ\text{C}$
Operating Junction Temperature, Continuous operation (under switching)	T_{jop}	-40 to 150	$^\circ\text{C}$
Maximum Case Temperature ^{*1}	T_{cmax}	125	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 125	$^\circ\text{C}$
Mounting Torque, M5 Mounting Screws	—	3.5	N-m
Terminal Connection Torque, M6 Terminal Screws	—	4.5	N-m
Module Weight (Typical)	—	420	Grams
Isolation Voltage	V_{ISO}	4000	Volts

^{*1} Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink under the chips.

^{*2} Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(MAX)}$ rating.

^{*3} Junction temperature (T_{vj}) should not increase beyond $T_{j(MAX)}$ rating.

DC Characteristics, $T_j=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain Source Leakage Current	I_{DSS}	$V_{DS}=1700V$, $V_{GS}=-15V$	-	-	1.0	mA
Gate Source Leakage Current	I_{GSS}	$V_{DS}=0V$, $V_{GS}=15V$	-	-	0.5	μA
Gate Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=10V$, $I_D=75\text{mA}$	1.8	-	3.2	Volts
Drain Source On-Resistance (chip)	$R_{DS(on)}$	$V_{GS}=15V$ $I_D=200A$	-	7.3	11.3	mΩ
		$T_j=150^\circ\text{C}$	-	10.74	-	mΩ
Internal Gate Source Series Resistance	R_g	Per Switch	-	0.75	-	Ω
Stray Inductance	L_s	P-N	-	15	-	nH

Dynamic Characteristics, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C _{ISS}		-	18.3	-	nF
Output Capacitance	C _{OSS}	V _{GS} =0V, V _{DS} =10V	-	11.3	-	nF
Reverse Transfer Capacitance	C _{RSS}		-	0.65	-	nF
Turn-On Delay Time	t _{D(on)}	V _{DD} =900V, V _{GS} = ±15V	-	200	-	ns
Rise Time	t _R	I _D =200A, R _G =1Ω, T _J =150°C	-	50	-	ns
Turn-Off Delay Time	t _{D(off)}	Inductive Load, per Pulse	-	220	-	ns
Fall Time	t _F		-	30	-	ns
Turn-On Energy	E _{on}	V _{DD} =900V, V _{GS} = ±15V	-	10.7	-	mJ
Turn-Off Energy	E _{off}	I _D =200A, R _G =1Ω, T _J =150°C Inductive Load, per Pulse	-	3.3	-	mJ
Total Gate Charge	Q _G	V _{DD} =900V, V _{GS} =0 to 15V I _D =200A, T _J =25°C	-	533	-	nC

Anti-parallel Diode, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Diode Forward Voltage	V _{SD}	V _{GS} =-15V I _S =200A	-	1.64	-	V
		T _J =150°C	-	2.52	-	V

Thermal Resistance Characteristics

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	R _{th(j-c)}	Per MOSFET	-	-	0.181	°C/W
Thermal Resistance, Junction to Case	R _{th(j-c)}	Per Diode	-	-	0.196	°C/W
Contact Thermal Resistance	R _{th(c-s)}	Per Module, Thermal Grease Applied λ=0.9 W/(mK)	-	0.015	-	°C/W

NTC Thermistor Part

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Zero Power Resistance	R ₂₅	T _C =25°C	4.85	5.00	5.15	kΩ
Deviation of Resistance	ΔR/R	T _C =100°C, R ₁₀₀ =493Ω	-7.3	-	+7.8	%
B constant	B _(25/50)	B _(25/50) =ln(R ₂₅ /R ₅₀) / (1/T ₂₅ - 1/T ₅₀) ⁻¹⁴	—	3375	—	K
Power Dissipation	P ₂₅	T _C =25°C	—	—	10	mW

*4 R25: Resistance at Absolute Temperature T25 (K), R50: Resistance at Absolute Temperature T50 (K), T25 = 25(°C) + 273.15 = 298.15(K), T50 = 50(°C) + 273.15 = 323.15(K)

