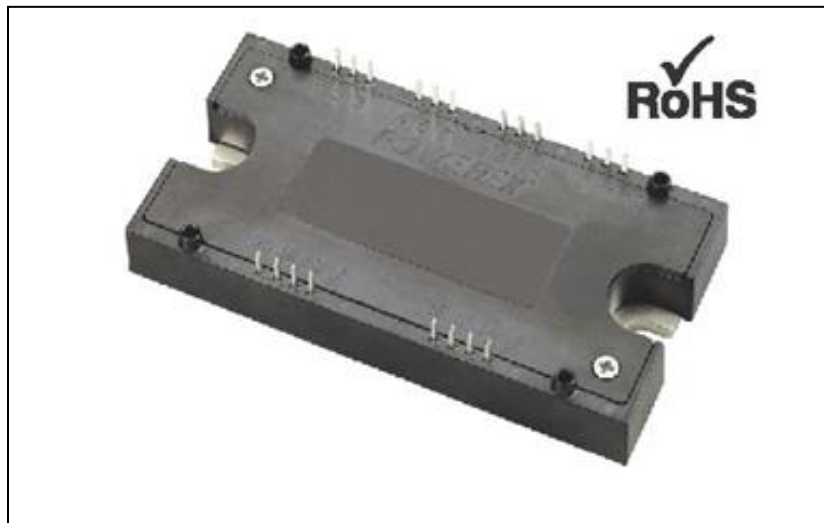
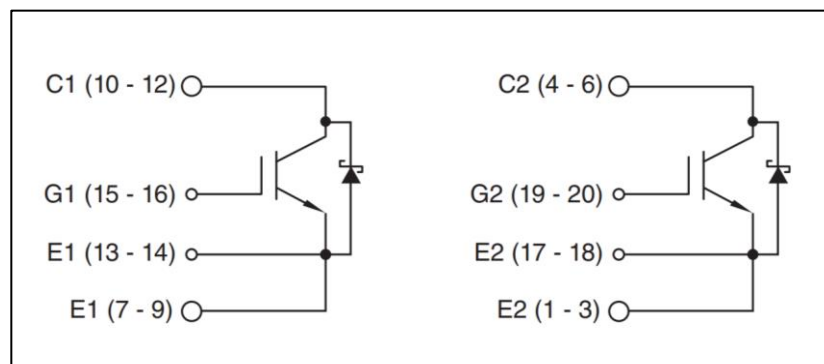


**Split Dual Si/SiC
Hybrid IGBT Module
100 Amperes / 1200 Volts****Split Dual Hybrid IGBT Module
100 Amperes / 1200 Volts****Description:**

Powerex IGBT Modules are designed for use for frequency up to 20 kHz. Each module consists of two IGBT Transistors with each transistor having a reverse connected super-fast recovery free-wheel silicon carbide Schottky diode. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- ☐ Low Switching Losses
- ☐ **Super-Fast Recovery**
Free-Wheel Silicon Carbide
Schottky Diode
- ☐ 2 Individual Switches per Module
- ☐ High Power Density
- ☐ Isolated Baseplate
- ☐ Aluminum Nitride Isolation

Applications:

- ☐ Energy Saving Power Systems
- ☐ High Frequency Type Power Systems
- ☐ High Temperature Power Systems

Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	QID1210SA1	Units
Operating Junction Temperature	T_{jop}	-40 to 150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 150	$^\circ\text{C}$
Collector-Emitter Voltage (G-E Short)	V_{CES}	1200	Volts
Gate-Emitter Voltage (C-E Short)	V_{GES}	± 20	Volts
Collector Current ($T_C = 25^\circ\text{C}$)	I_C	100*	Volts
Peak Collector Current	I_{CM}	200*	Amperes
Emitter Current** ($T_C = 25^\circ\text{C}$)	I_E	67*	Amperes
Repetitive Peak Emitter Current ($T_C = 25^\circ\text{C}$, $t_p = 10\text{ms}$, Half Sine Pulse)**	I_{EM}	135*	Amperes
Maximum Collector Dissipation ($T_C = 25^\circ\text{C}$, $T_j \leq 150^\circ\text{C}$)	P_C	937	W
Maximum Case Temperature* ¹	$T_{c\ max}$	150	$^\circ\text{C}$
Maximum Junction Temperature	$T_{j\ max}$	175	$^\circ\text{C}$
Mounting Torque, M6 Mounting Screws	—	5	Nm
Module Weight (Typical)	—	270	Grams
Isolation Voltage	V_{ISO}	3500	Volts

*1 Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink under the chips.

*2 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j\ (MAX)}$ rating.

*3 Junction temperature (T_j) should not increase beyond $T_{j\ (MAX)}$ rating.

DC Characteristics, $T_j=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain Source Leakage Current	I_{CES}	$V_{CE}=1200\text{V}$, $V_{GE}=0\text{V}$	-	-	1.0	mA
Gate Source Leakage Current	I_{GES}	$V_{CE}=0\text{V}$, $V_{GE}=\pm 20\text{V}$	-	-	0.5	μA
Gate Source Threshold Voltage	$V_{GE(th)}$	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$	5.4	6.0	6.6	Volts
Collector-Emitter Saturation Voltage (chip)	$V_{CE(sat)}$	$I_C = 100\text{A}$, $V_{GE} = 15\text{V}$, $T_j = 25^\circ\text{C}$	-	1.55	1.8	Volts
		$I_C = 100\text{A}$, $V_{GE} = 15\text{V}$, $T_j = 125^\circ\text{C}$	-	1.75	-	Volts
		$I_C = 100\text{A}$, $V_{GE} = 15\text{V}$, $T_j = 150^\circ\text{C}$	-	1.80	-	Volts
Stray Inductance	L_s	P-N	-	10	-	nH

Dynamic Characteristics, $T_J=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C_{ies}	$V_{CE}=10\text{V}, V_{GE}=0\text{V}$	-	22.8	-	nF
Output Capacitance	C_{oes}		-	0.8	-	nF
Reverse Transfer Capacitance	C_{res}		-	0.3	-	nF
Turn-On Delay Time	$t_{d(on)}$	$V_{CC}=600\text{V}, V_{GE}=\pm 15\text{V}$ $I_C=100\text{A}, R_G=3.9\Omega$ Inductive Load	-	100	-	ns
Rise Time	t_r		-	50	-	ns
Turn-Off Delay Time	$t_{d(off)}$		-	250	-	ns
Fall Time	t_f	$V_{CC}=600\text{V}, V_{GE}=\pm 15\text{V}$ $I_C=100\text{A}, R_G=3.9\Omega, T_J=150^\circ\text{C}$ Inductive Load	-	150	-	ns
Turn-On Energy	E_{on}		-	1.7	-	mJ
Turn-Off Energy	E_{off}		-	4.5	-	mJ
Recovery Energy	E_{rec}		-	0.3	-	mJ
Total Gate Charge	Q_G	$V_{CC}=600\text{V}, V_{GE}=\pm 15\text{V}, I_C=100\text{A}$	-	0.7	-	μC

Anti-parallel SiC Shottky Diode, $T_J=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Capacitive Charge	Q_C	$V_{CC}=600\text{V}, V_{GE}=\pm 15\text{V}, I_D=67\text{A}$	-	TBD	-	μC
Diode Forward Voltage	V_{EC}	$V_{GE}=0\text{V}, I_E=67\text{A}$	-	1.53	-	V
		$T_J=125^\circ\text{C}$	-	2.05	-	V

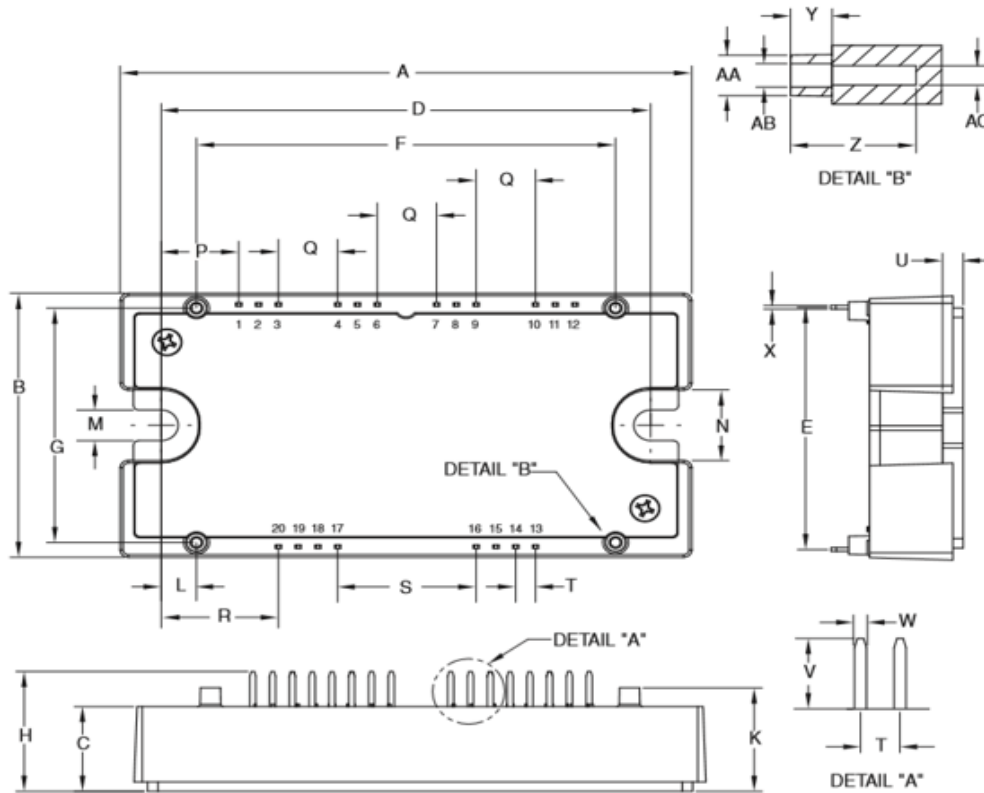
Thermal Resistance Characteristics

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	$R_{th(j-c)}$	Per IGBT, $\frac{1}{2}$ Module	-	-	0.16	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{th(j-c)}$	Per Diode, $\frac{1}{2}$ Module	-	-	0.62	$^\circ\text{C/W}$
Contact Thermal Resistance	$R_{th(c-s)}$	Per $\frac{1}{2}$ Module, Thermal Grease Applied	-	0.04	-	$^\circ\text{C/W}$

NTC Thermistor Part

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Zero Power Resistance	R_{25}	$T_C=25^\circ\text{C}$	4.85	5.00	5.15	k Ω
Deviation of Resistance	$\Delta R/R$	$T_C=100^\circ\text{C}, R_{100}=493\Omega$	-7.3	-	+7.8	%
B constant	$B_{(25/50)}$	$B_{(25/50)}=\ln(R_{25}/R_{50}) / (1/T_{25} - 1/T_{50})^{*4}$	—	3375	—	K
Power Dissipation	P_{25}	$T_C=25^\circ\text{C}$	—	—	10	mW

*4 R25: Resistance at Absolute Temperature T25 (K), R50: Resistance at Absolute Temperature T50 (K), $T_{25} = 25(^\circ\text{C}) + 273.15 = 298.15(\text{K})$, $T_{50} = 50(^\circ\text{C}) + 273.15 = 323.15(\text{K})$



Dimensions	Inches	Millimeters
A	4.32	109.8
B	2.21	56.1
C	0.71	18.0
D	3.70±0.02	94.0±0.5
E	2.026	51.46
F	3.17	80.5
G	1.96	49.8
H	1.00	25.5
K	0.87	22.0
L	0.266	6.75
M	0.26	6.5
N	0.59	15.0
P	0.586	14.89

Dimensions	Inches	Millimeters
Q	0.449	11.40
R	0.885	22.49
S	1.047	26.6
T	0.15	3.80
U	0.16	4.0
V	0.30	7.5
W	0.045	1.15
X	0.03	0.8
Y	0.16	4.0
Z	0.47	12.1
AA	0.17 Dia.	4.3 Dia.
AB	0.10 Dia.	2.5 Dia.
AC	0.08 Dia.	2.1 Dia.