

# 1200V/900A Half Bridge SiC MOSFET Module



## **Description**

The PRXS900HF12I4C1 is a Half Bridge SiC MOSFET Power Module. It integrates high performance SiC MOSFET chips designed for the applications such as Motor drives and Renewable energy.

#### **Features**

- □ Blocking Blocking voltage 1200V
- $R_{DS(on)} = 2.0 \text{m}\Omega$
- ☐ Low thermal resistance with Si<sub>3</sub>N<sub>4</sub> AMB
- □ 175°C maximum junction temperature
- □ Thermistor inside
- □ Low Switching Losses

## **Applications**

- □ xEV Applications
- □ Motor Drives
- □ Vehicle Fast Chargers
- □ Smart-Grid/Grid-Tied Distributed Generation

## **Circuit Diagram**

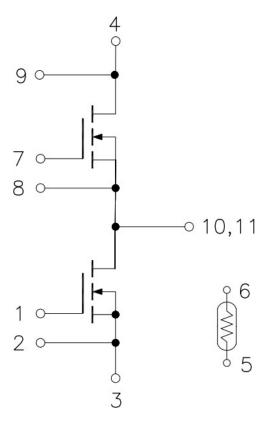


Figure 1. Out drawing & circuit diagram for PRXS900HF12I4C1



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## **Pin Configuration and Marking Information**

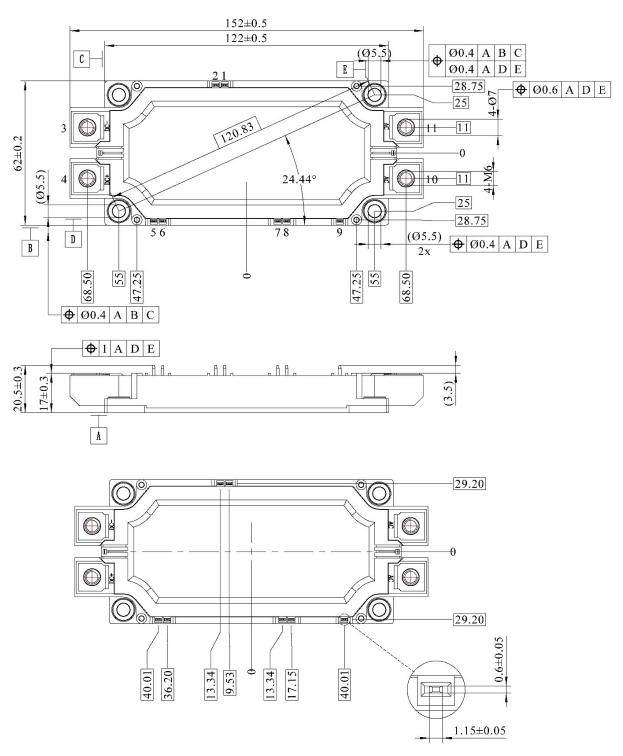


Figure 2. Pin configuration



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#### **Module**

Parameter	Conditions	Value	Unit	
Isolation Voltage	RMS, f=50Hz, t=1min	3.4	kV	
Material of module baseplate	-	Cu	-	
Creepage distance	terminal to heatsink terminal to terminal	14.5 13	mm	
Clearance	terminal to heatsink terminal to terminal	12.5 10	mm	
CTI	-	>400	-	
Module lead resistance, terminals-chip	T <sub>C</sub> =25° C	0.5	mΩ	
Mounting torque for module mounting	M5, M6	3 to 6	Nm	
Weight	-	340	g	

## **Maximum Ratings** $(T_j = 25^{\circ}C \text{ unless otherwise specified})$

Symbol	Parameter	Condition	Ratings	Unit
V <sub>DSS</sub>	Drain-Source Voltage	G-S Short	1200	V
V <sub>GSS</sub>	Gate-Source Voltage	D-S Short, AC frequency ≥1Hz, Note1	-10 to 22	V
$I_{DS}$	DC Continuous Drain Current	$T_{\rm C} = 25$ °C, $V_{\rm GS} = 18$ V	1000	A
I <sub>DS</sub>	DC Continuous Drain Current	$T_{\rm C} = 60^{\circ}  {\rm C, V_{GS}} = 18 {\rm V}$	900	A
$I_{SD}$	Source (Body diode) Current	T <sub>C</sub> =25°C, with ON signal	1000	A
$I_{SD}$	Source (Body diode) Current	T <sub>C</sub> =60°C, with ON signal	900	A
I <sub>DSM</sub>	Pulse Forward Current	$T_C = 25$ °C, Pulse width =1 ms, $V_{GS} = 20$ V, Note2	1800	A
P <sub>tot</sub>	Total Power Dissipation	T <sub>C</sub> =25°C	3000	W
T <sub>jmax</sub>	Max Junction Temperature	-	175	° C
$T_{stg}$	Storage Temperature	-	-40 to 125	° C

Note1: Recommended Operating Value, +18V/-5V, +18V/-4V, +15V/-4V

Note2: Pulse width limited by maximum junction temperature

## **NTC Characteristics**

Symbol	Parameter	C . IV		Value		
		Condition	Min.	Тур.	Max.	Unit
R <sub>25</sub>	Resistance	T <sub>C</sub> =25° C	-	5	-	kΩ
ΔR/R	Deviation of R <sub>100</sub>	$T_{\rm C} = 100^{\circ}  {\rm C}, R_{100} = 493\Omega$	-5	-	5	%
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25° C	-	-	20	mW
B <sub>25/50</sub>	B-value	R2 =R25 exp [B <sub>25/50</sub> (1/T2 - 1/(298,15 K))]	-	3375	-	K
B <sub>25/80</sub>	B-value	R2 =R25 exp [B <sub>25/80</sub> (1/T2 - 1/(298,15 K))]	-	3411	-	K
B <sub>25/100</sub>	B-value	R2 =R25 exp [B <sub>25/100</sub> (1/T2 - 1/(298,15 K))]	-	3433	-	K



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# **MOSFET Electrical characteristics** (T<sub>j</sub> =25°C unless otherwise specified, chip)

Cbl	T4		C . Pr		Value			Unit
Symbol	Item	Condition			Min.	Тур.	Max	Un
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 900\mu A$			1200	-	-	7
$I_{DSS}$	Zero gate voltage drain Current	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0				30	-	μ
		I <sub>D</sub> =315mA	T <sub>i</sub> =25° (		1.8	2.7	_	,
$V_{\text{GS(th)}}$	Gate-source threshold Voltage	$V_{DS} = V_{GS}$	T <sub>i</sub> =175°		-	2.1	-	,
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS} = 20V, V_{DS} = 0V,$	T <sub>j</sub> =25° C		-	-	900	n
		I <sub>D</sub> =900A	T <sub>i</sub> =25° C	2	-	2.5	3.3	n
D (GI:)	Static drain-source	$V_{GS} = +15V$	T <sub>i</sub> =175°		-	3.5	-	n
R <sub>DS(on)</sub> (Chip)	On-state resistance	I <sub>D</sub> =900A	T <sub>j</sub> =25° C	2	-	2.0	-	n
		$V_{GS} = +18V$	T <sub>j</sub> =175°	С	-	2.8	-	r
		I <sub>D</sub> =900A	T <sub>j</sub> =25° C	2	-	2.25	2.97	
V (Chin)	Static drain-source	$V_{GS} = +15V$	$T_j = 175^{\circ}$	С	-	3.15	-	
V <sub>DS(on)</sub> (Chip)	On-state Voltage	I <sub>D</sub> =900A	T <sub>j</sub> =25° C	2	-	1.80	-	
		$V_{GS}$ =+18V	T <sub>j</sub> =175°	C	-	2.52	-	
$C_{iss}$	Input Capacitance				-	52.2	-	
C <sub>oss</sub>	Output Capacitance	$V_D = 1000V, V_{GS} = 0V$	nV		-	1.60	-	
$C_{rss}$	Reverse transfer Capacitance	1-200KHz, v <sub>AC</sub> -231	f=200kHz, V <sub>AC</sub> =25mV			0.13	-	
$R_{Gint}$	Internal gate resistor	-				0.7	-	
$Q_G$	Total gate charge	$V_{DD} = 800V, I_D = 540A, V_{GS} = +18/-5V$				1890	-	
				T <sub>j</sub> =25° C	-	51	-	
$t_{d(on)}$	Turn-on delay time			T <sub>i</sub> =150° C	-	47	-	ns
			_		-	29	-	
$t_r$	Rise time			$T_j = 25^{\circ} \text{ C}$ $T_j = 150^{\circ} \text{ C}$	_	27	-	ns
			-		_	116	-	
$t_{d(off)}$	Turn-off delay time	$V_{DD} = 600V$ $I_{D} = 900A$		$T_j = 25^{\circ} \text{ C}$ $T_j = 150^{\circ} \text{ C}$	_	132	-	1
		$V_{GS} = +18/-4V$		T <sub>j</sub> =25° C	-	25	-	
$\mathbf{t}_{\mathrm{f}}$	Fall time Inductive load switching operation	Inductive load switching operation		T <sub>j</sub> =150° C	-	42	-	1
E <sub>on</sub>	Turn-on power dissipation			T <sub>i</sub> =25° C	_	35.5	-	
				T <sub>i</sub> =150° C	_	41.7	_	1
				T <sub>i</sub> =25° C	_	18.5	_	
$\rm E_{off}$	Turn-off power dissipation				_	21.8	_	1
R <sub>th(j-c)</sub>	FET Thermal Resistance	Junction to Case		T <sub>j</sub> =150° C	_	0.05	-	K
	Contact thermal Resistance	With thermal conductive grease, Note3			_	0.015		K

Note3: Assumes Thermal Conductivity of grease is 0.9W/m·K and thickness is 50um.



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## **Body Diode Electrical Characteristics** (T<sub>i</sub> =25°C unless otherwise specified, chip)

Cl 1	14	Condition		Value			TI .*4
Symbol	Item			Min.	Typ.	Max	Unit
V	Body Diode Forward Voltage	$V_{GS} = -5V$ $I_{SD} = 900A$	T <sub>j</sub> =25° C	-	6.3	-	V
$V_{SD}$			T <sub>i</sub> =175° C	-	5.6	-	
т	Reverse recovery time	$\begin{aligned} &V_{DD}{=}600V\\ &I_{D}{=}900A\\ &V_{GS}{=}{+}18/{4}V\\ &R_{gon}{'}R_{goff}{=}3.3/2.2\Omega\\ &Inductive\ load\\ &switching\ operation \end{aligned}$	T <sub>i</sub> =25° C	-	29	-	ns
T <sub>rr</sub>	Reverse recovery time		$T_i=150^{\circ} C$	-	47	-	
	Q <sub>rr</sub> Reverse recovery charge		$T_i=25^{\circ} C$	-	3.5	-	_
Q <sub>rr</sub>			T <sub>j</sub> =150° C	-	13.2	-	uС
E <sub>rr</sub>	Diode switching power dissipation		T <sub>j</sub> =25° C	-	1.82	-	I
			T <sub>j</sub> =150° C	-	5.65	-	mJ

## **Test Conditions**

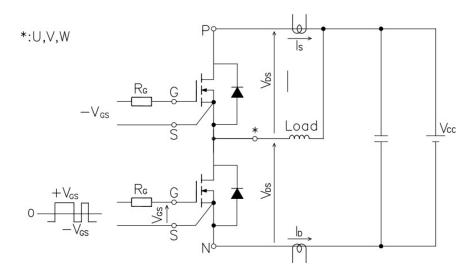


Figure 3. Switching time measure circuit

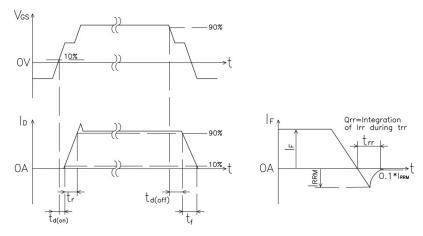
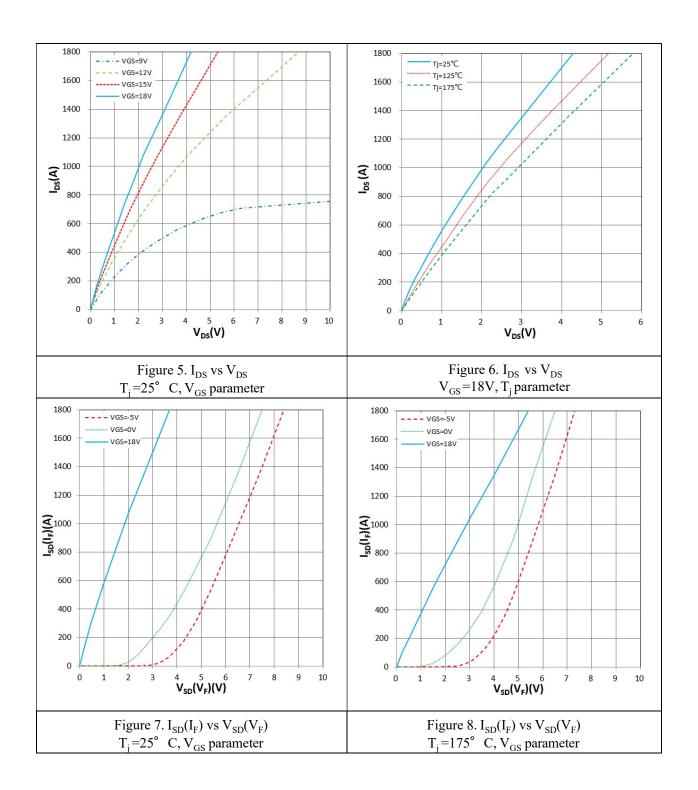


Figure 4. Switching time definition

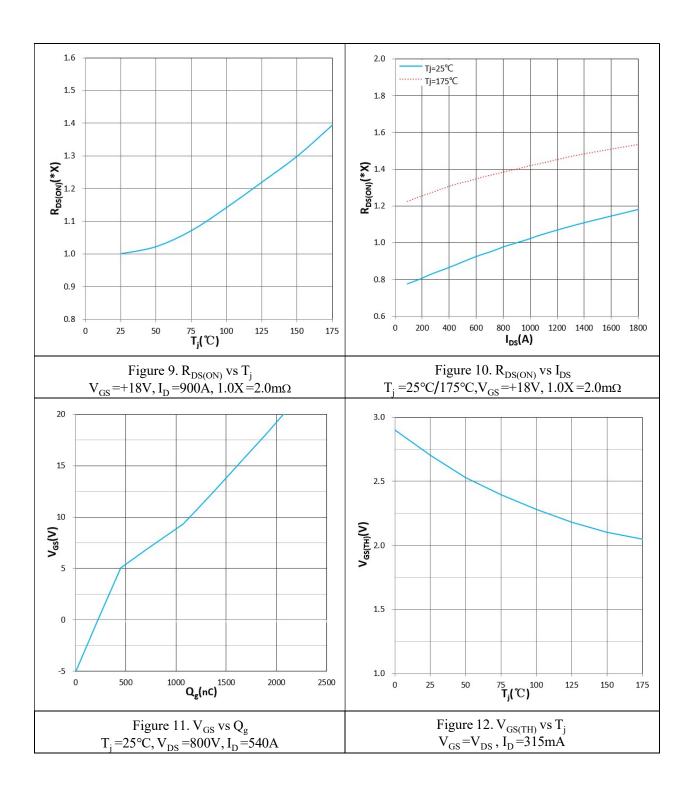


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