

DESCRIPTION

M54567P and M54567FP are four-circuit Darlington transistor arrays with clamping diodes. The circuits are made of PNP and NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

- High breakdown voltage ($BV_{CEO} \geq 50V$)
- High-current driving ($I_{C(max)} = 1.5A$)
- With clamping diodes
- Driving available with NMOS IC output
- Wide operating temperature range ($T_a = -20$ to $+75^\circ C$)

APPLICATION

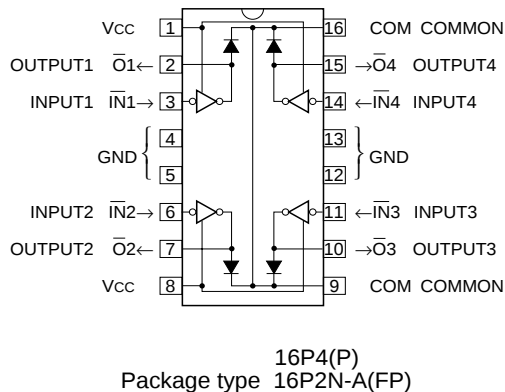
Drives of relays and printers, digit drives of indication elements (LEDs and lamps), and power amplification

FUNCTION

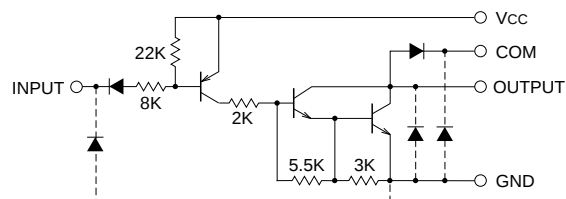
The M54567P and M54567FP each have four circuits, which are made of PNP transistors and NPN Darlington transistors. The input has $8k\Omega$, and a spike-killer clamping diode is provided between the output pin (collector) and COM pin. All output transistor emitters are connected to the GND pin. Collector current is 1.5A maximum. The maximum collector-emitter voltage is 50V.

The M54567FP is enclosed in a molded small flat package, enabling space-saving design.

PIN CONFIGURATION



CIRCUIT DIAGRAM



The four circuits share the COM and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit : Ω

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, $T_a = -20 \sim +75^\circ C$)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CC}	Supply voltage		10	V
V_{CEO}	Collector-emitter voltage	Output, H	$-0.5 \sim +50$	V
I_C	Collector current	Current per circuit output, L	1.5	A
V_I	Input voltage		$-0.5 \sim +30$	V
V_R	Clamping diode reverse voltage		50	V
I_F	Clamping diode forward current	Pulse Width $\leq 10ms$, Duty Cycle $\leq 5\%$	1.5	A
		Pulse Width $\leq 100ms$, Duty Cycle $\leq 5\%$	1.0	
P_d	Power dissipation	$T_a = 25^\circ C$, when mounted on board	1.92(P)/1.00(FP)	W
T_{opr}	Operating temperature		$-20 \sim +75$	$^\circ C$
T_{stg}	Storage temperature		$-55 \sim +125$	$^\circ C$

4-UNIT 1.5A DARLINGTON TRANSISTOR ARRAY WITH CLAMP DIODE

RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted, $T_a = -20 \sim +75^\circ\text{C}$)

Symbol	Parameter		Limits			Unit
			min	typ	max	
VCC	Supply voltage		4	5	6	V
Vo	Output voltage		0	—	50	V
IC	Collector current (Current per 1 circuit when 4 circuits are coming on simultaneously)	VCC = 5V, Duty Cycle P : no more than 4% FP : no more than 2%	0	—	1.25	A
		VCC = 5V, Duty Cycle P : no more than 18% FP : no more than 9%	0	—	0.7	
VIH	“H” input voltage		VCC–0.5	—	VCC	V
VIL	“L” input voltage		0	—	VCC–3.5	V

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, $T_a = -20 \sim +75^\circ\text{C}$)

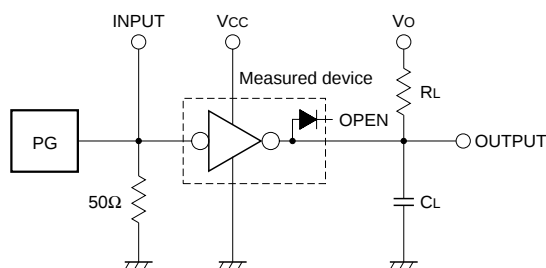
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_{CEO} = 100\mu\text{A}$	50	—	—	V
I_{CC}	Supply current (One circuit coming on)	$V_{CC} = 6\text{V}$, $V_I = 0.5\text{V}$	—	3.0	4.5	mA
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{CC} = 4\text{V}$, $V_I = 0.5\text{V}$, $I_C = 1.25\text{A}$	—	1.6	2.2	V
		$V_{CC} = 4\text{V}$, $V_I = 0.5\text{V}$, $I_C = 0.7\text{A}$	—	1.1	1.7	
I_i	Input current	$V_I = V_{CC}-3.5\text{V}$	—	-0.3	-0.6	mA
		$V_I = V_{CC}-6\text{V}$	—	-0.58	-0.95	
I_R	Clamping diode reverse current	$V_R = 50\text{V}$	—	—	100	μA
V_F	Clamping diode forward voltage	$I_F = 1.25\text{A}$, V_{CC} open	—	1.6	2.3	V
h_{FE}	DC amplification factor	$V_{CC} = 4\text{V}$, $V_{CE} = 4\text{V}$, $I_C = 1\text{A}$, $T_a = 25^\circ\text{C}$	4000	30000	—	—

* : The typical values are those measured under ambient temperature (T_a) of 25°C . There is no guarantee that these values are obtained under any conditions.

SWITCHING CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

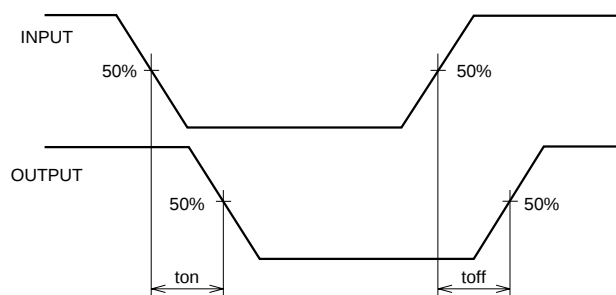
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
t_{on}	Turn-on time	$C_L = 15\text{pF}$ (note 1)	—	190	—	ns
t_{off}	Turn-off time		—	5300	—	ns

NOTE 1 TEST CIRCUIT



- (1) Pulse generator (PG) characteristics : PRR = 1kHz,
 $t_w = 10\mu\text{s}$, $t_r = 6\text{ns}$, $t_f = 6\text{ns}$, $Z_o = 50\Omega$
 $V_I = 0.5$ to 4V
- (2) Input-output conditions : $R_L = 8.3\Omega$, $V_o = 10\text{V}$, $V_{CC} = 4\text{V}$
- (3) Electrostatic capacity C_L includes floating capacitance at connections and input capacitance at probes

TIMING DIAGRAM



TYPICAL CHARACTERISTICS

